

**Listing of the Claims**

Claims 1-40 (Cancelled).

5        41. (Currently Amended) An emitter, comprising:

            a flat emitting surface having a first area;

            a first chamber having substantially parallel sidewalls interfacing to  
the emitting surface;

            a second chamber interfacing to the first chamber and having

10      sidewalls diverging to an opening having a second area larger than the first area;  
and

            a cathode layer disposed [[on]]over both the emitting surface and sidewalls  
of the first and second chambers.

15        42. (Previously Presented) The emitter of claim 41, wherein the emitter

has been subjected to an annealing process thereby increasing the emission  
capability of the emitter.

20        43. (Original) The emitter of claim 41 wherein the first chamber is formed  
within an adhesion layer.

44. (Original) The emitter of claim 41 wherein the second chamber is  
formed within a conductive layer.

25        45. (Original) An integrated circuit comprising at least one emitter of claim  
41.

46. (Original) A display device comprising at least one emitter of claim 41.

30        47. (Original) A storage device comprising at least one emitter of claim 41.

Claims 48-71 (Cancelled).